

Docket No. 211200US2



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Toshitada SAITO

SERIAL NO: 09/900,945

GAU: 2133

RCE FILED: Herewith

EXAMINER: TRIMMINGS, J. P.

FOR: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT INCLUDING PROCESSOR CIRCUIT  
AND ITS PERIPHERAL CIRCUITS

REQUEST FOR EXTENSION OF TIME  
UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

It is hereby requested that a two month extension of time be granted to March 29, 2005 for

- ☐ filing a response to the Official Action dated:
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☒ filing a Request for Continued Examination (RCE). A timely response to the final rejection, due January 29, 2005 was filed on December 29, 2004.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$450.00 is enclosed herewith by credit card payment and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

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